Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	20606	interface with((((within single internal$ system)near2(chip ic(integrated circuit)semiconductor))soc)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/26 18:33
L2	5012	(reuse$ reusabl$ reusing)near6(block module logic)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/26 17:24
L3	237	2 and(system adj chip)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/26 17:24
L4	112	3 and(@ad<"19991102" @prad<"19991102" @rlad<"19991102")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/26 18:43
L5	87	1 and 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/26 18:33
L6	36372	internal$ near5 bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/26 18:51
L7	28974	(conver$ translat$)near7 bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/26 18:41
L8	1608	6 same 7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/26 18:41
L9	4286	(block logic module)with 7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/26 18:51

L10	113	1 and 8 and 9	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/26 18:43
L11	82	10 and(@ad<"19991102" @prad<"19991102" @rlad<"19991102")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/26 18:55
L12	268774	(function$2 interface socket configurable reconfigurable)near3(block logic module)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/26 18:55
L13	158	8 same 12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/26 18:55
L14	130	13 and(@ad<"19991102" @prad<"19991102" @rlad<"19991102")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/26 18:55